General	Title and code of	ETR 575- Automated design of e	lectronic devices- 8	
information	subject, number of	ECTS credits		
	credits			
	Department	Physics and Electronics		
	Program	Bachelor		
	Academic semester	2023 Spring		
	Lecturer	PhD, Assistant Professor Elchin	Hasanov	
	E-mail:	elgafgas@yahoo.com		
	Phone number:	+994 50 5287740		
	Lecture	11 Mehseti Street, AZ1096 Baku	ı, Azerbaijan (Neftchilar	
	room/Schedule	campus), room		
	Consultations	II, 15:00 – 16:00		
	Office hours	Sunday 10-00		
Prerequisites	EENG 245			
Course	English			
language				
Type of the	Major			
Subject	Touthooka			
1 extbooks	1 Kozontowy Vu M. Chor	toy A.S. Design of electronic day	ices in the environment of	
allu	1. Kazantsev Yu.M., Chertov A.S. Design of electronic devices in the environment of			
matorials	house 2000-102s	CE, TOLOCE. Teaching ald.	- Tomsk. ITO publishing	
materials	1 Nouse, 2000-1028.			
	2. Kazanisev 1 u.ivi. Guidennes for the preparation of initial data for the database of the PSPICE circuit design software package. Method, directions to			
	conducting a lab. slave. Ed. TPU, 1995 13s			
	3. Kazantsev Yu.M. Operating manual for the circuit software package			
	design "PSPICE" Method. instructions for the lab. slave. Ed. TPU. 1995 23s.			
	4. Kazantsev Yu.M. Calculation of typical circuits and components of converter			
	technology devices. Metho	od. instructions for the lab. slave. I	Ed. TPU, 1995 44s.	
	5. Kazantsev Yu.M. Methods and programs for machine calculation of elements and			
	units of VIP.			
	Method. instructions for th	ne lab. slave. Ed. TPU, 1995 75s	. Logistics of discipline	
Teaching	Lecture	•	+	
methods	Group discussions at sen	ninars	+	
Assessment	Components	Date/ Deadline	Percent (%)	
		During the semester	<u> </u>	
	Active participation	At each lesson	10	
	Quizzes	During the semester	15	
	Attendance Midtarma avam	During the semester	3	
	Final avam		30	
	Final exam		55 100	
Course	rillal Quality indicators of an	thut parameters of electronic devi		
description	Quanty mulcators of ou	uput parameters of electronic devi-	UCS.	
acscription	- Design of nowar alastron	nice devices		
	- Design of power electron	nics devices	nverters	
	 Design of power electron Algorithms for designing voltage, inverters and rect 	nics devices g and optimizing circuits of DC co ifiers	nverters	

	- Design analysis of electromagnetic compatibility		
	- Study of the circuit, measurement of the electrical modes of the elements, determination		
	integral values of output characteristics		
	- Development and approval of the program for the study of the circuit on the model.		
	Analysis and		
Course	The purpose of the discipline is:		
obiectives	Goal 1. In the field of education - to acquaint students with typical software products		
U U	focused on solving scientific, design and technological problems of electronics, to teach the principles and methods of calculation, design and construction of components, devices and electronic devices based on a systematic approach, including the stages of a circuit, design and technological design, the requirements for		
	standardization of technical documentation, teach how to apply methods and computer systems for the design and study of devices and devices of electronic equipment		
	Goal 2. In the field of education - to teach how to work effectively individually and in a team, to demonstrate the skills necessary for professional and personal		
	development; Goal 3. In the field of development - preparing students for the further development of new professional knowledge and skills, self-study, continuous professional self- improvement		
Learning	As a result of mastering the discipline, the student must:		
outcomes	Know the terms and definitions of the design process, the basic principles and		
	methods of organizing CAD, basic modeling methods,		
	Be able to evaluate the integral parameters of the device and the electrical modes of circuit elements, develop a program and methodology for testing electronic devices on the developed models.		
	Own methods and algorithms for designing electronic devices based on standard design procedures, methods for analyzing design results, skills in working with special literature.		
	In the process of mastering the discipline, students develop the following competencies:		
	1. general cultural -		
	the ability to adapt to changing conditions, to overestimate the accumulated		
	experience, analyze their capabilities		
	2 General professional -		
	the ability to use the results of mastering the disciplines of the master's program the ability to independently acquire and use in practice new knowledge and skills in their subject area		
	3. Professional -		

	- research activities:
	the ability to develop effective algorithms for solving formulated problems using
	modern programming languages and ensure their software implementation
	- design and development activities:
Rules (Educational policy and behavior)	 Preparation for the lesson. This course makes your study and preparation outside of the classroom essential. Lectures are based on what is presented in the text. A visual explanation will greatly help your understanding of the lecture. After the lecture, you should study your notes and work through the relevant tasks and cases from the end of the chapter and the sample exam questions. Withdrawal (pass/fail) This course strictly follows grading policy of the School of Humanities, Education and Social sciences. Thus, a student is normally expected to achieve a mark of at least 60% to pass. In case of failure, he/she will be required to repeat the course thefollowing term or year. Cheating/plagiarism Plagiarism or epigonism during midterm and final exams will result in the cancellation of the work. In this case, the student automatically gets zero (0), without any reasoning Rules of professional conduct Students must behave appropriately for the university in order to create an appropriate aura during their studies. Unauthorized discussions and unethical behavior are strictly prohibited. Attendance Students who attend the whole classes will get 5 marks. for three absence student loses 1 mark. Quizzes There will be a quizzes per two weeks. The quizzes will be announced in the classroom two weeks before and will relate to homework. Activity Students who will be active during discussion of past lessons will be awarded with one activity mark.

This program reflects the comprehensive information about the subject and information about any changes will be provided in advance.

Wee k	Dates (planned)	Subject topics	Textbook/ Assignments
1	18. 02. 23	Computer-Aided Design Methodology Basic concepts and principles of computer-aided design	[1] p.709-741
2	25.02/23	Modeling of electronic devices The content aspect of the design process. Stages and stages of creating new technology, design procedures and operations. Block-hierarchical approach to design. Hierarchy of system structures Problem solving	[1] p.750-773 [1] p-780- 795

3	04/03/23	Design of power electronics devices;	[1] p.800-815
		Diagnostic and control tests, principles of their generation when using computer-aided design systems	
		Problem solving.	
4	11/03/23	Design of devices of low-current electronics Basic software products, solutions of design problems in electronic engineering.	[1] p.815-838
		Problem solving.	
5	18/03/23	.Construction and technological design and reliability assurance	
		The main tasks to be solved at the modeling stages	
		Problem solving.	
6	25/03/23	Methods for computer-aided design of secondary power sources	[1] p.846-872
		Models of circuit components and principles of their formation in circuit modeling systems	
		Problem solving.	
7	01/04. 23	Power electronic systems of autonomous objects	[1]p.881- 900,
		Functional-logical design of digital units of electronic circuits.	
		Asynchronous and synchronous modeling. Signal and Element Models	
		Problem solving	
8	08/04/23	Machine methods of analysis and design of electronic circuits. Functional design. Basic elements of functional circuits and	[1] p.957- 984
		algorithms for their modeling.	
		Problem solving.	
00	15/04/22	Mid term exam	[1] = 0.16 0.047
09	13/04/23	design of radio electronic devices	[1] p.910-947
		Problem solving	

10	22/04/23	Building mathematical models of analog electronic circuits in	[4]p. 73-96
		the frequency and time domain	
		Problem solving	
11	29/04/23	Construction of mathematical models of assembly-switching spaces of structures in the form of graphs	[1] p.1061- 1085
		Problem solving	
12	06/05. 23	Building a model of electronic means using modern applied tools for computer-aided design	[3]p.555-611
		Problem solving.	
13	13/05. 23	Construction of a formalized model of a part for the synthesis of the technological process of its manufacture.	[1] page 9-6
		. Problem solving	[1] page 10- 6
14	20. 05/23	Automated synthesis of control systems for electronic devices Diagnostic and control tests, principles of their generation when using computer-aided design systems	[1] page 11- 6
15	27.05.23	Closing the feedback loop, verifying the model Design and technological design and ensuring the reliability of the developed equipment	[1] page 14- 21

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