Identification	Subject	ETR 234 Analog Electronics- 6 ECTS credit	ts		
	(code, title, credits)	-			
	Department	Physics and Electronics			
	Program	Undergraduate			
	(undergraduate,				
	graduate)				
	Term	Spring, 2023			
	Instructor	Ahmad Asimov ph.D			
	E-mail:	-			
	Phone:	+994124211093 (daxili255)			
	Classroom/hours	302N Monday/Wednesday			
D	Office hours	Tuesday: 15:00-16:00/ Thursday: 15:00-16	:00		
Prerequisites	English				
Language Compulsory	English Compulsory				
Required textbooks and	<u> </u>	s Adel S Sedra and Kenneth C Smith (	Ovford University Press 5th		
course materials	Edition, 2004	Microelectronic Circuits, Adel S. Sedra and Kenneth C. Smith, Oxford University Press, 5th			
course materials		Circuit Theory, Robert L. Boylestad and Lou	is Nashelsky. Pearson Higher		
	Electronic Devices and Circuit Theory, Robert L. Boylestad <i>and</i> Louis Nashelsky, Pearson Education, 10th Edition 2009				
	· ·	nventional current version (9th ed.), Floy	d, T. L.,Pearson Education,		
	Prentice Hall,2012 (Oth	ner references are provided is class as we	ll) Introduction to Electrical		
	Engineering, Mutukula Sa	ırma,Oxford			
Course Description		a basic understanding of the fundamentals an			
	and electronic devices in electrical and electronic engineering. This understanding is a critical step				
	_	esign new electronic circuits or use them app	propriately as part of a larger		
	engineering system.				
	<u> </u>				
Course objectives	The aims of this course are to:				
	<ul> <li>Introduce students to concept of semiconductor materials and semiconductor devices</li> <li>Provide students with the working knowledge of diodes and their applications</li> <li>Provide students with the working knowledge of transistors and their applications</li> </ul>				
Learning outcomes	Upon successful complet	tion of this course, students will be able to:			
C		epts of semiconductor materials and semico	onductor devices. 2. Analyse		
		ors circuits and their biasing 3. Analyse varie			
	circuits 4. Use the theory and applications of semiconductor devices for creating electronic circuits 5. Understand the operation principle of oscillators. Learn to calculate the control of the control				
		and oscillation frequency of different oscill			
packages and lab experiments for testing and analyzing different electronics circular Teaching methods  Lecture					
reaching methods	Group discussion		<u> </u>		
	Experiential exercise		<u> </u>		
	Quizzes and Homework		$\overline{\square}$		
	<b>Q</b>				
Evaluation	Methods	Date/deadlines	Percentage (%)		
	Midterm Exam		30		
	Case studies				
	Class Participation	At each lesson	5		
	Quizzes	During the semester	20		
	Activity	During the semester	5		
	Presentation		10		
	Final Exam		40		
	Others		100		
	Total		100		

#### **Policy**

# Preparation for class

The structure of this course makes your individual study and preparation outside the class extremely important. The lecture material will focus on the major points introduced in the text. Reading the assigned chapters and having some familiarity with them before class will greatly assist your understanding of the lecture. After the lecture, you should study your notes and work relevant problems and cases from the end of the chapter and sample exam questions.

#### • Withdrawal (pass/fail)

This course strictly follows grading policy of the School of Science and Engineering. Thus, a student is normally expected to achieve a mark of at least 60% to pass. In case of failure, he/she will be required to repeat the course the following term or year.

# Cheating/plagiarism

Cheating or other plagiarism during the Quizzes, Mid-term and Final Examinations will lead to paper cancellation. In this case, the student will automatically get zero (0), without any considerations.

### Professional behavior guidelines

The students shall behave in the way to create favorable academic and professional environment during the class hours. Unauthorized discussions and unethical behavior are strictly prohibited.

# Quizzes

 There will be a quiz examination per two weeks. The quizzes will be announced in the classroom two weeks before. Quiz is from homework problems.

The homework problems will be selected from questions and problems in the end of each chapter. The No. of homework problems will be announced after finishing each chapter.

#### Attendance

Students who attend the whole classes will get 5 marks. for two absence student loses 1 mark.

### Activity

Students who will be active during discussion of past lessons will be awarded with one activity mark.

	Schedule Topics Textbook					
Week	(tentative)	Topics	Textbook			
1	14.02.23 16.02.23	Introduction, semiconductor materials and diodes.	Chapter 1			
2	21.02.23 23.02.23	Diodes and Diodes Circuits	Chapter 2			
3	28.02.23 01.03.23	Diodes and Diodes Circuits	Chapter 3			
4	7.03.23 9.03.23	The Bipolar Junction Transistor	Chapter 4			
5	14.03.23 16.03.23	BJTs circuits	Chapter 5			
6	28.03.23 30.03.23	BJTs circuits	Chapter 5			
7		Midterm exam				
8	04.04.23 06.04.23	Transistor Amplifiers	Chapter 6			
9	11.04.23	FETs Transistors	Chapter 7			

Tentative

	12.04.22		
	13.04.23		
10	18.04.23	FETs Transistors	Chapter 8
	20.04.23		
11	25.04.23		Chapter 9
	27.04.23	FETs circuits	
12	02.05.23	FETs circuits	Chapter 9
	04.05.23		
13	11.05.23	FETs circuits	Chapter 10
	16.05.23		
14	18.05.23	OP-AMPs	Chapter 10
14	23.05.23	OI -AIVII S	Chapter 10
15	25.05.23	OP-AMPs	Chapter 11
	30.05.23	OP-AMP applications	
		Of -Aivir applications	
		Final Exam	

